



FEATURE

- 1Phase(1 I/O), 3Phases(3 I/O)
- Precision measurement even for distorted waveform
- Self powered or Loop powered models available
- Output signal programmable by dip-switch
- Low output ripple
- High impulse & Surge protection
- High stability & low cost
- CE

SPECIFICATION

INPUT: Current or Voltage

| | AC Input | | Input Burden | Input Frequency | |
|---------|-----------------------------|-------------------------|--------------|-----------------|-------------|
| | | | | | |
| Current | Aux. Powered & Loop Powered | 0 ~ 1 A | ≦0.10VA | 50 Hz ±3 Hz | 60 Hz ±3 Hz |
| | | 0 ~ 5 A | | | |
| | Self Powered | 20%-100% of input range | ≦1.50VA | 50 Hz ±1 Hz | 60 Hz ±1 Hz |
| | | 0 ~ 10 A | | | |
| Voltage | Aux. Powered & Loop Powered | 0 ~ 150 V | ≦0.15VA | 50 Hz ±3 Hz | 60 Hz ±3 Hz |
| | | 0 ~ 300 V | | | |
| | Self Powered | 0 ~ 500 V | ≦4.00VA | 50 Hz ±1 Hz | 60 Hz ±1 Hz |
| | | 20%-100% of input range | | | |

OUTPUT: Current or Voltage O/P Programming by Dip Switch inside

| Output Range | Load Resistance | Output Resistance | Output Ripple | Self-powered units can not be used for 4-20mA, 1-5V and 2-10V output. |
|------------------------|-------------------------------|-------------------|---------------|---|
| 0 ~ 1 V | ≧500Ω | ≧0.001Ω | ≦0.2% F.S. | |
| 0 ~ 5 V | ≧500Ω Self Powered: ≧2KΩ | | | |
| 0 ~ 10 V | ≧1000Ω Self Powered: ≧2KΩ | | | |
| 1 ~ 5 V | ≧500Ω | | | |
| 0 ~ 1 mA | 0 ~ 12KΩ | ≧20MΩ | | |
| 0 ~ 10 mA | 0 ~ 1200Ω Self Powered: ≧500Ω | | | |
| 0 ~ 20 mA | 0 ~ 600Ω Self Powered: ≧500Ω | ≧6MΩ | | |
| 4 ~ 20 mA | 0 ~ 600Ω | | | |
| Loop Powered 4 ~ 20 mA | Vs / (20 mA) - 900Ω | | | |

※When Aux Powered is DC The Load Resistance is about 70%

Accuracy : ≦ ±0.2% of F.S. ; Self Powered ≦ ±0.2% of F.S

Waveform effect ≦ 0.2% of F.S. at 30% distortion

Max. input over capability: Voltage: 1.5 x rated continuous
2 x rated for 10 seconds
4 x rated for 2 seconds

Current: 3 x rated continuous
10 x rated for 10 seconds
50 x rated for 1 second

Response time: ≦250 mS

Span adjustment: ≦ ±5% of F.S. (or ±20% of F.S. specify)

Zero adjustment: ≦ ±2% of F.S. (or ±20% of F.S. specify)

Output load effect: Current output ≦0.1% of F.S.

Voltage output ≦0.05% of F.S.

Power supply: ADH : AC 85-264V · DC 100-300V

ADL : AC / DC 20-56V

Loop powered DC 18 ~ 32V

Self Powered: Not required

Power effect: ≦0.05% F.S.

Power consumption: ≦10 VA(1P2W) ; ≦12 VA(3P3W)

Mutual interference effect: ≦0.1% R.O. between each element

Magnetic field strength: 400ATM ≦0.2% F.S.

Operating temperature: 0-60 °C

Operating relative: 20-95 %RH, non-condensing

Temperature coefficient: ≦100 PPM/°C

Storage temperature: -10-70 °C

Dielectric Strength: IEC 414, IEC 688:1992, ANSI C37.90a

Between Input / Output / Power / Case

Surge test:

AC 4KV, 50/60Hz, 1 min.

IEC 255-4, ANSI C37.90a

6KV, 1.2 x 50 μsec

Common mode & differential mode

Insulation resistance:

≧100M ohm, DC 500V

Safety:

IEC 414, BS 5458

Enclosure:

IEC 529 (IP50)

Certification Standard

IEC 60688 (Except 3P3W)

CE:

EMC:EN61326:2003

Safety(LVD):

EN61010:2001

Mounting:

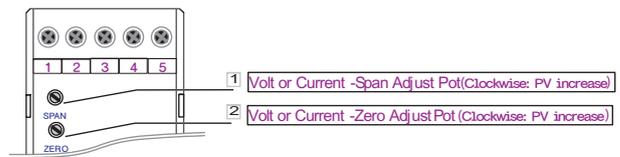
Wall or DIN rail (EN 50022)

Weight:

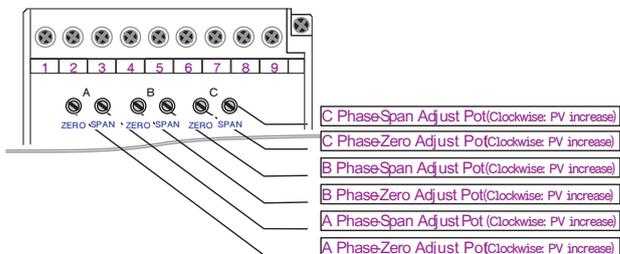
1P: under 450g, 3P: under 650g

ADJUSTMENT

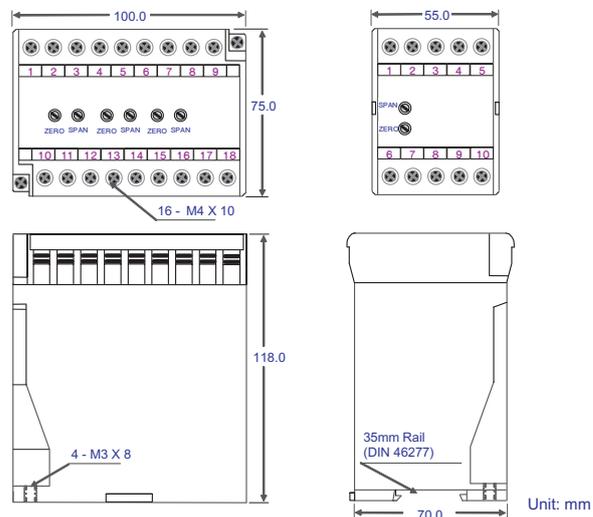
Volt or Current – 1 Phase



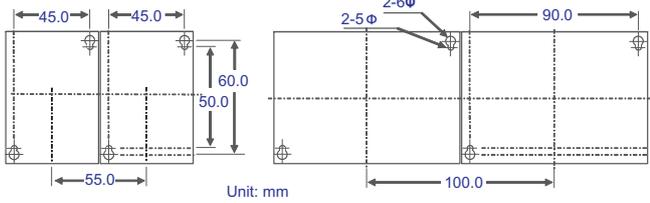
Volt or Current – 3 Phases



DIMENSIONS



PANEL MOUNTING HOLES



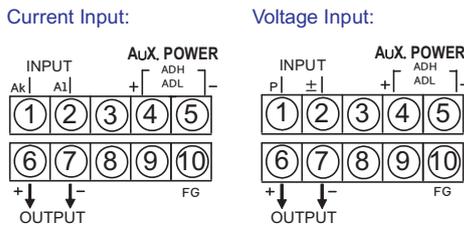
OUTPUT RANGE PROGRAMMING

| OUTPUT | Dip Switch | | | | | | | |
|-----------|------------|----|----|----|----|----|----|----|
| | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| 0 ~ 1 mA | | | | | on | | | |
| 0 ~ 10 mA | | | | | on | on | | |
| 0 ~ 20 mA | | | | | on | | on | |
| 4 ~ 20 mA | on | | | | on | | on | |
| 0 ~ 1 V | | on | on | on | | | | on |
| 0 ~ 5 V | | | on | on | | | | on |
| 0 ~ 10 V | | | | on | | | | on |
| 1 ~ 5 V | on | | on | on | | | | on |

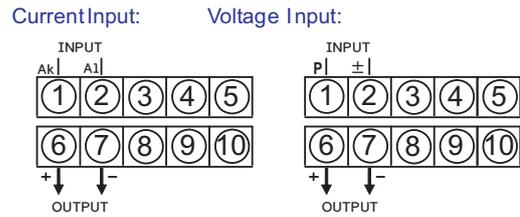
* Pads: blank fields mean open.

CONNECTION DIAGRAM

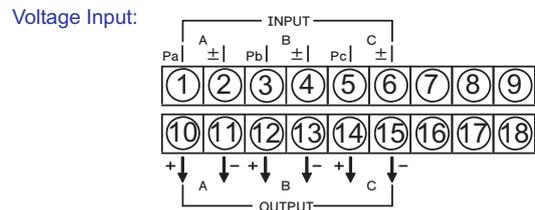
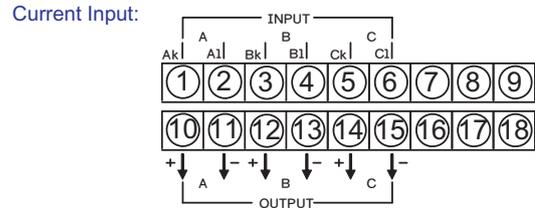
1 Phase (Auxiliary Powered)



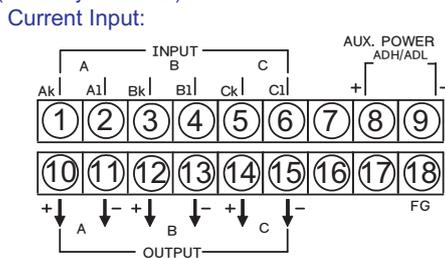
1 Phase (Self Powered)



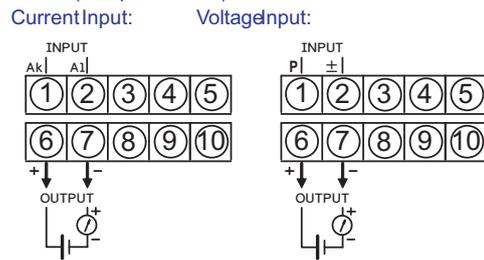
3 Phases (Self Powered)



3 Phases (Auxiliary Powered)



1 Phase (Loop Powered)



ORDERING INFORMATION

